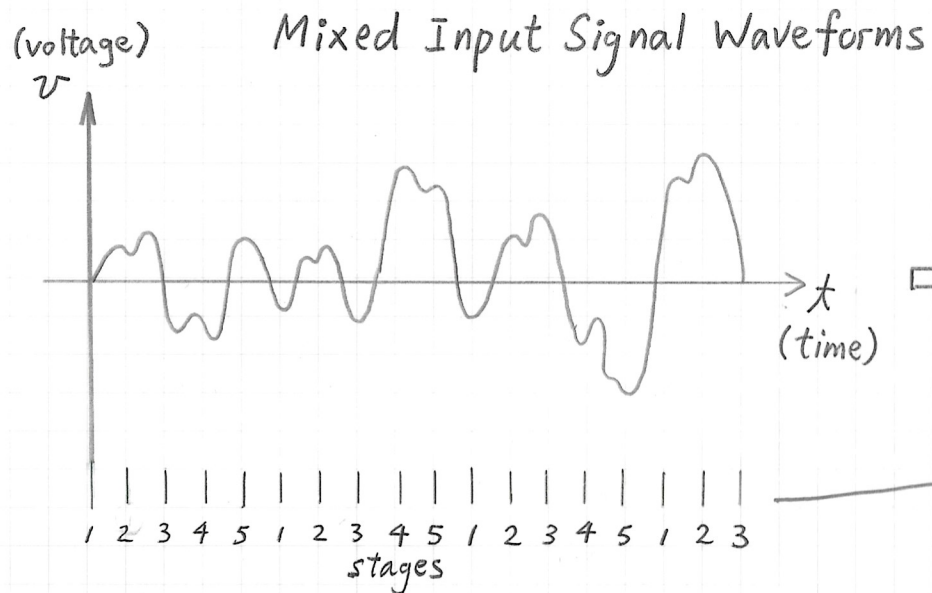


Interleaved Signal Sampling

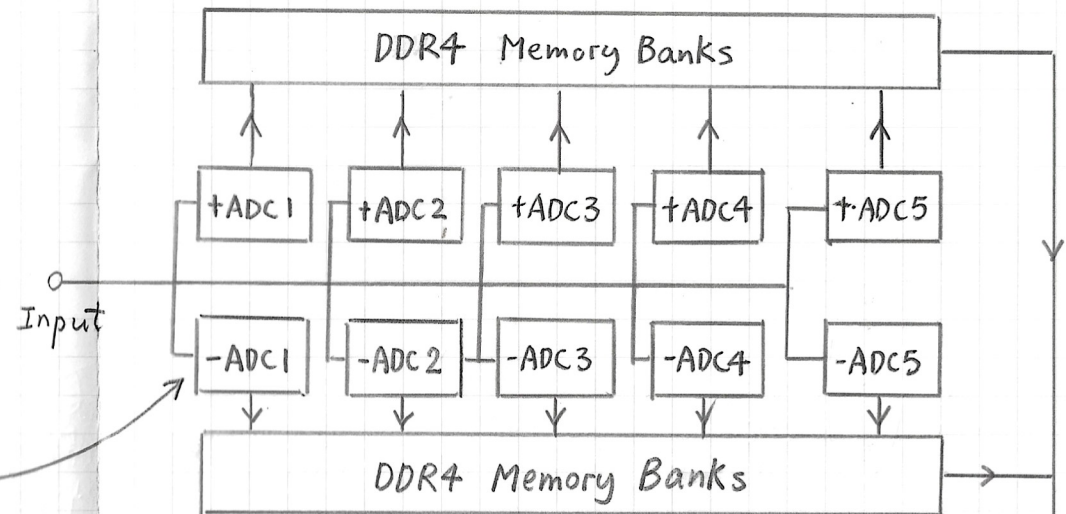


With several (e.g. 5) successive stages of analog-to-digital conversions, we can enhance the limiting sampling rate (currently 9 GSPS) by multiples as we desire.

Johnny Fung July 18, 2020
Hong Kong (852) 6767-7761

Signal Sampling

DDR4 - 4400
peak transfer 35.2 GB/s



- Successively each pair of ADC (analog-to-digital converters) take samples of the input waveforms
- Currently the fastest ADC is the Texas Instrument's AFE 7422 with capability of 14-bit at 9 GSPS [giga sample per second]

